

Appl. No. : 08/942,333  
Filed : October 1, 1997

32. The system of Claim 31, wherein the remote interface includes a power source independent of a power source for the second computer.

33. The system defined in Claim 31, wherein the remote interface includes an external port for connection to the first computer.

34. The system defined in Claim 31, wherein the first computer is at the same location as the second computer.

35. The system defined in Claim 31, wherein the first computer is at a location remote to the second computer.

36. The system defined in Claim 35, additionally comprising a pair of modems, wherein a first modem connects to the first computer and a second modem connects to the second computer via the remote interface, and wherein the first modem is in data communication with the second modem.

37. The system defined in Claim 36, wherein each modem further connects to the public switched telephone network.

38. The system defined in Claim 36, wherein each modem further connects to a cable network.

39. The system defined in Claim 36, wherein each modem facilitates connection to a satellite.

40. The system defined in Claim 31, wherein the remote interface includes a remote interface microcontroller that connects via a bus to the microcontroller.

41. The system defined in Claim 31, wherein the remote interface executes code associated with a command sent from the first computer to reset the second computer.

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42. The system defined in Claim 31, wherein the processor of the second computer comprises a central processing unit.

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43. The system defined in Claim 31, wherein the microcontroller is a general purpose microcontroller.

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44. The system defined in Claim 31, additionally comprising:  
a plurality of other microcontrollers, wherein the remote interface includes one of the plurality of other microcontrollers; and  
a microcontroller bus interconnecting the plurality of other microcontrollers and the microcontroller.

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45. The system defined in Claim 31, wherein the remote interface is configured to provide secure access to the second computer from the first computer.

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46. The system defined in Claim 31, wherein the remote interface is directly connected to and proximately located to the second computer.

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47. The system defined in Claim 32, wherein the independent power source powers the second computer when the second computer power source is inoperable or operating below a threshold power level.

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48. The system defined in Claim 31, wherein the remote interface is capable of communicating the results of the reset command to the first computer.

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49. The system defined in Claim 48, wherein the remote interface comprises a circuit having a remote interface microcontroller and a remote interface memory, and program code stored in the memory, and wherein the remote interface memory is connected to the remote interface microcontroller and stores the result data.

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50. A system for managing the conditions of a computer, the system comprising:  
a computer including a central processing unit;

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a microcontroller bus; and

a plurality of microcontrollers that are interconnected by the microcontroller bus and wherein the microcontrollers manage the conditions of the computer, and wherein a selected one of the microcontrollers resets the central processing unit via a direct signal path.

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51. The system defined in Claim 50, additionally comprising a remote interface, wherein the remote interface includes another selected one of the microcontrollers.

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52. The system defined in Claim 51, wherein the remote interface includes a remote interface power source independent of a power source for the computer.

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53. The system defined in Claim 51, wherein the remote interface is configured to provide secure access to the computer from an external computer.

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54. A system for resetting a computer, the system comprising:  
a recovery manager executing on a first computer;  
a microcontroller bus;  
a plurality of microcontrollers that are interconnected by the microcontroller bus, wherein at least one of the plurality of microcontrollers is configured to cause a reset;  
a second computer including a central processing unit, wherein the recovery manager sends a reset command to the central processing unit via the at least one of the plurality of microcontrollers.

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55. The microcontroller system of Claim 54, wherein the recovery manager includes a graphical user interface capable of obtaining information utilized in sending the reset command.

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56. The microcontroller system of Claim 54, wherein one of the microcontrollers is a remote interface microcontroller.

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57. The microcontroller system of Claim 56, wherein the remote interface microcontroller provides a data communication path between the microcontroller bus and the recovery manager.

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~~58.~~ The microcontroller system of Claim <sup>26</sup>~~56~~, wherein the remote interface microcontroller receives power from a remote interface power source which is independent of a main power source for the second computer.

<sup>24</sup>  
~~59.~~ The microcontroller system of Claim <sup>28</sup>~~58~~, wherein the independent power source connected to the remote interface microcontroller provides power to the second computer when the main power source fails.

<sup>30</sup>  
~~60.~~ The microcontroller system of Claim <sup>26</sup>~~56~~, wherein the remote interface microcontroller is directly connected to and proximately located to the second computer.

<sup>31</sup>  
~~61.~~ A system for resetting a computer, the system comprising:  
a first computer;  
a remote interface in data communication with the first computer;  
a microcontroller connected to the remote interface; and  
a second computer in data communication with the remote interface, the second computer comprising a plurality of processors, wherein the first computer sends a reset command to the microcontroller via the remote interface, and wherein the microcontroller is configured to provide a reset signal to reset less than all of the plurality of processors at the second computer.

<sup>32</sup>  
~~62.~~ A system for resetting a computer, the system comprising:  
a first computer;  
a remote interface in data communication with the first computer;  
a microcontroller connected to the remote interface; and  
a second computer in data communication with the remote interface, the second computer comprising a central processing unit, wherein the first computer sends a reset command to the microcontroller via the remote interface, and wherein the microcontroller is configured to provide a reset signal directly to the central processing unit so as to reset the central processing unit at the second computer.

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